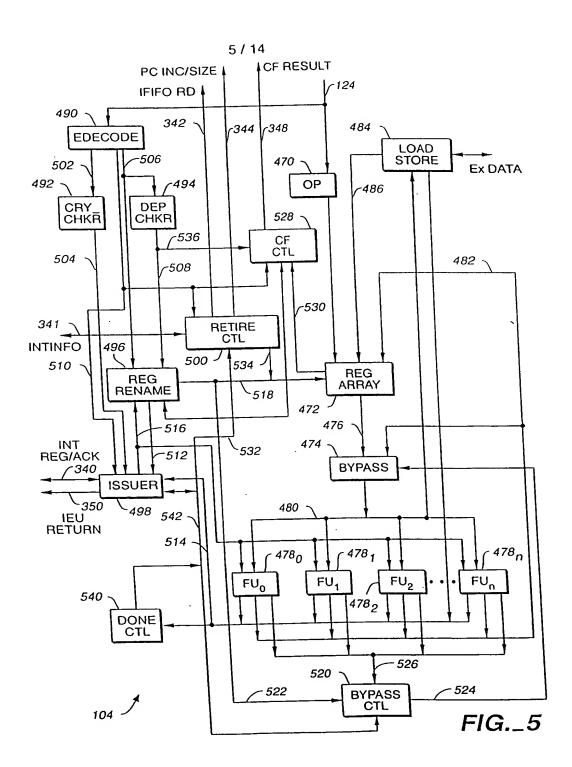
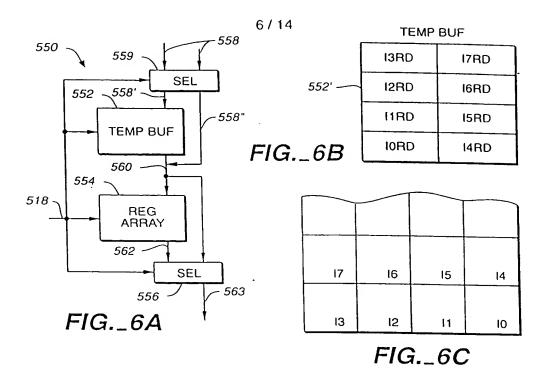
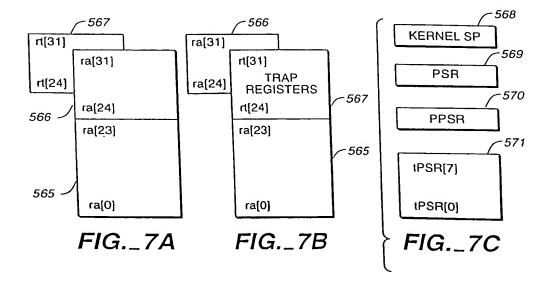
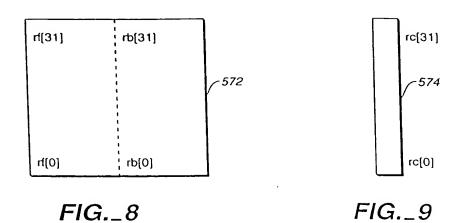


...



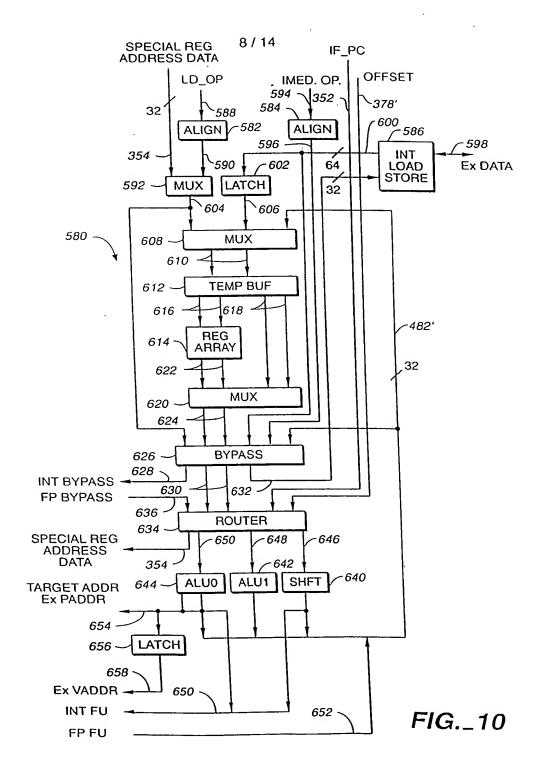


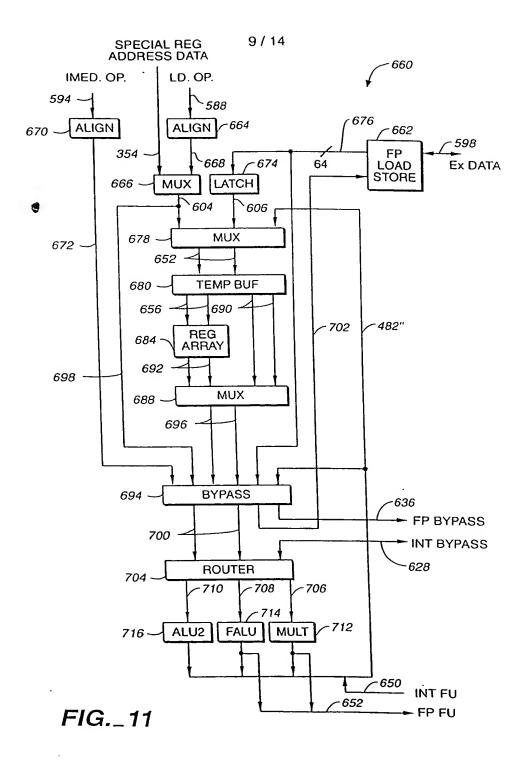




- 106 -860 → MCU ADDR [13:4] VM PADDR — 524 28 [31:14] -132 <u>r 862</u> ← MCU CTL + ID [3:0] ICACHE IF ID IN - 296 [3:0] <u> 136</u> MCU IBUS IF DATA IF IREAD -IF IBUSY - 300 IF IRDY - 302 -864 - MCU ADDR 134 EXIDIN 796 32 _______ MCU CTL + ID **DCACHE** Ex DATA 792 MCU DBUS 80 Ex REQ Ex BUSY -Ex RDY Ex R/W CCU Ex WIDTH -FIG._17

784





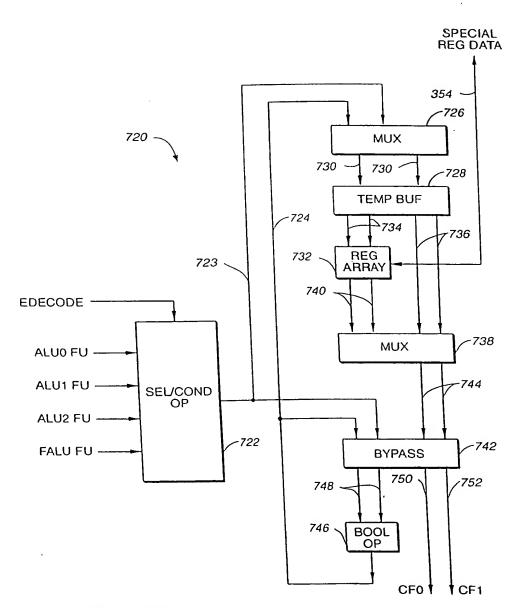


FIG._12

